Accurate Inductance Modeling of 3-D Inductor Based on TSV

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Abstract—In this letter, an accurate model for the inductance of 3-D integrated inductor based on through silicon via is proposed. The model, considering both the internal inductance and detailed calculation of redistribution layer inductance, can more precisely obtain the total inductance of 3-D inductor using negligible computational time. Compared with the results of the Q3D extractor, the inductance results obtained from the proposed model exhibit good agreement with various design parameter ranges. The maximum error is less than 3.5%, so our model achieves high accuracy.

Index Terms—3-D integrated inductor, inductance model, redistribution layers (RDL), through silicon via (TSV).

I. INTRODUCTION

S A critical enabling technique for 3-D integrated circuits, the through silicon via (TSV) can provide high aspect ratio interconnection for multilayer chips in the vertical direction to realize the integration of heterogeneous chips [1]. Moreover, TSVs with redistribution layers (RDLs) are also developed in multiple applications such as various integrated passive devices [2]. Thus, it is a new choice to realize 3-D on-chip inductors using TSVs and RDLs. Compared with traditional planar inductors, the TSV-based inductors can occupy less die area and get a higher inductance density by making use of the vertical direction.

At present, considerable research has been conducted on the TSV-based 3-D inductor. However, the inductance value of the inductor is obtained by the empirically approximated expressions based on Y-parameters from the 3-D full-wave simulation in most works [3], which requires a lot of time and computational resources. Hence, the analytical method is required to calculate the inductance value. Wang and Yu [4] proposed a simple inductance model of 3-D inductor according to the self and mutual inductance of TSVs and RDLs. Due to many approximations are used in this method, large errors are introduced, especially when the size of the inductor is large.

In Section II, we developed a precise inductance model based on geometries for the total inductance of the inductor.

II. INDUCTANCE MODEL DESCRIPTION

Referred to the implemented 3-D inductors [3], the physical structure of TSV-based 3-D inductor (four turns) is shown in Fig. 1(a), where l_{TSV} and r_{TSV} denote the length and radius of TSV. The width and thickness of RDL are denoted by w_{RDL} and t_{RDL} . The conductor segments of the inductor can

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Fig. 1. (a) Structure and design parameters of TSV-based 3-D inductor (four turns). (b) Top view of top-RDL of the inductor.

be divided into four groups: TSVs from the same row, TSVs from different rows, RDLs from the top layer (top-RDL), and the RDLs from the bottom layer (bottom-RDL). As shown in Fig. 1(a), P_{TSV} and S_{TSV} denote the distances between TSVs from different rows and the same row, respectively.

The partial self-inductance of an individual TSV of radius r and length of l is expressed as in (1), which is developed using the magnetic flux density [5]. Considering that a 3-D inductor of N turns contains 2N TSVs which are connected in series, the total self-inductance of TSVs can be derived from (2). Where μ is the permeability of the surrounding medium, here the surrounding materials of TSV are not ferromagnetic, so $\mu = \mu_0 = 4 \times 10^{-7}$ is permeability of the vacuum in this letter

$$L_{S1}(l,r) = \frac{\mu}{2\pi} l \left[\ln\left(\frac{l}{r} + \sqrt{\left(\frac{l}{r}\right)^2 + 1}\right) - \sqrt{\left(\frac{r}{l}\right)^2 + 1} + \frac{r}{l} + \frac{1}{4} \right]$$
(1)

$$L_{\text{TSV},S} = 2NL_{S1}(l_{\text{TSV}}, r_{\text{TSV}}) \tag{2}$$

$$L_{S2}(l, a, b) = \frac{\mu}{2\pi} l \left[\ln \left(\frac{2l}{a+b} \right) + \frac{1}{2} + \frac{a+b}{3l} \right] + \frac{\mu}{8\pi}$$
(3)

$$L_{\text{RDL},S} = NL_{S2}(l_{\text{RDL},b}, w_{\text{RDL}}, t_{\text{RDL}}) + (N-1)L_{S2}(l_{\text{RDL},t}, w_{\text{RDL}}, t_{\text{RDL}}) + 2L_{S2}(l_{\text{RDL},t}/2, w_{\text{RDL}}, t_{\text{RDL}}) + 2L_{S2}(l_{\text{RDL},io}, w_{\text{RDL}}, t_{\text{RDL}}).$$
(4)

The partial self-inductance of an individual RDL interconnect is expressed as in (3), where $\mu l/8\pi$ is internal inductance of the RDL. The internal inductance is considered in this formula because the magnetic fields are existed both inside and outside the conductor. Where *l* is the length of the RDL, *a* and *b* are dimensions of the rectangular cross section. There are several different lengths of RDLs in an *N*-turns 3-D inductor, included that *N* bottom-RDLs of length $l_{\text{RDL},b} = P_{\text{TSV}}$, (*N*-1) top-RDLs of length $l_{\text{RDL},t} = (P_{\text{TSV}}^2 + S_{\text{TSV}}^2)^{1/2}$, two top-RDLs of length $l_{\text{RDL},t}/2$ and two top-RDLs used as input or output of signal (IO-RDL) of length $l_{\text{RDL},io}$, which

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is defined in Fig. 1(a). Similarly, the RDLs in inductor are connected in series, so the total self-inductance of RDLs can be determined by (4).

The partial mutual inductance between two TSVs can be calculated using (5), where *l* is the length of the TSV and *d* is the distance between the two TSVs. Thus, the total mutual inductance between TSVs from the same row in the 3-D inductor can be determined by (6), where $d_{ij} = (j - i)S_{\text{TSV}}$ is the distance between the *i*th and *j*th TSVs from the same row

$$M_{p1}(l,d) = \frac{\mu}{2\pi} l \left[\ln\left(\frac{l}{d} + \sqrt{\left(\frac{l}{d}\right)^2 + 1}\right) - \sqrt{\left(\frac{d}{l}\right)^2 + 1} + \frac{d}{l} \right]$$
(5)

$$M_{\rm TSV,sr} = 2\sum_{i=1}^{N} \sum_{j=i+1}^{N} M_{p1}(l_{\rm TSV}, d_{ij})$$
(6)

$$M_{\rm TSV,dr} = \sum_{i=1}^{N} \sum_{j=1}^{N} M_{p1}(l_{\rm TSV}, d_{ij})$$
(7)

$$M_{\text{RDL},b} = \sum_{i=1}^{N-1} \sum_{j=i+1}^{N} M_{p1}(l_{\text{RDL},b}, d_{ij}).$$
(8)

In a similar way, the total mutual inductance between TSVs from different rows can be calculated by (7), where $d_{ij} = (P_{\text{TSV}}^2 + ((i - j)S_{\text{TSV}})^2)^{1/2}$ is the distance between the *i*th and *j*th TSVs from different rows.

For simplicity, the RDL is simplified as a cylinder when calculating the mutual inductance in some works [4]. Therefore, the total mutual inductance between bottom-RDLs can be calculated by (8). Where $d_{ij} = (j - i)S_{\text{TSV}}$ is the distance between the *i*th and *j*th bottom-RDL.

Fig. 1(b) shows the top view of the top-RDL in a 3-D inductor, as shown in Fig. 1(a). The mutual inductance between every two top-RDLs can be calculated by the formula of (9) and (10) [6]. Where l and m are lengths of two RDLs, respectively. The two RDLs have a center-to-center separation of d, and their endpoints are offset by a distance p. These parameters are all defined in Fig. 1(b).

The two IO-RDLs have a center-to-center distance of 0 and an offset of $p_N = NS_{TSV}$, which are approximately perpendicular to other RDLs, so the mutual inductance between IO-RDL and other RDLs is extremely small and can be ignored. The total mutual inductance between top-RDLs can be derived by (11), where *i* and *j* denote the *i*th and *j*th top-RDLs. For example, Fig. 1(b) shows the geometries of the second and fourth top-RDLs. l_{ij} and m_{ij} are lengths of two RDLs, here excepts $l_{1j} = m_{iN+1} = l_{\text{RDL},t}/2$, other $l_{ij} = l_{\text{RDL},t}$. The offset *p* and center-to-center distance *d* of the *i*th and *j*th top-RDLs can be calculated by (12) and (13).

Fig. 2 shows the view of two RDLs from different layers. The mutual inductance between these two RDLs can be derived by (14) [7], where $d = l_{\text{TSV}} + t_{\text{RDL}}$ is the distance between the top and bottom layers, l and m denote the length of two RDLs, R_1 , R_2 , R_3 , and R_4 are distances between every two endpoints of top-RDL and bottom-RDL, which are all defined in Fig. 2(a). The angle θ is identical between every two top-RDLs and bottom-RDLs, as shown in Fig. 2(b), which can be determined by the trigonometric function of P_{TSV} and S_{TSV} . The distance α and β between third top-RDL and first bottom-RDL are defined in Fig. 2(b). Ω is the function of d, α , β , θ , R_1 , R_2 , R_3 , and R_4 . So the total mutual inductance between RDLs from different layers can be derived by (15).



Fig. 2. (a) Perspective view. (b) Top view of two RDLs from different layers.

The distance α and β between the *i*th top-RDL and *j*th bottom-RDL can be determined by (16)

$$M(l,d) = \frac{\mu}{2\pi} \left(l \sinh^{-1} \left(\frac{l}{d} \right) - \sqrt{l^2 + d^2} + d \right)$$
(9)
$$M_{p2}(l,m,p,d)$$

$$= 0.5(M(p+m,d) + M(p-l,d) - M(m+p-l,d) - M(m+p-l,d) - M(p,d))$$
(10)

$$M_{\text{RDL},t} = \sum_{i=1}^{N} \sum_{j=i+1}^{N} M_{p2}(l_{ij}, m_{ij}, p_{ij}, d_{ij}) + M_{p2}(l_{\text{RDL},io}, l_{\text{RDL},io}, p_N, 0)$$
(11)

$$p_{ij} = (j-i) \cdot S_{\text{TSV}} \sin \theta = (j-i) \cdot \frac{S_{\text{TSV}}}{\sqrt{P_{\text{TSV}}^2 + S_{\text{TSV}}^2}}$$

$$d_{ij} = (j-i) \cdot S_{\text{TSV}} \cos \theta = (j-i) \cdot \frac{S_{\text{TSV}} P_{\text{TSV}}}{\sqrt{P_{\text{TSV}}^2 + S_{\text{TSV}}^2}}$$
(12)
(12)
(12)
(12)
(12)
(13)

$$M_{p3}(l, m, d, \alpha, \beta, \theta) = \frac{\mu}{2\pi} \cos \theta \left[(\alpha + l) \tanh^{-1} \frac{m}{R_1 + R_2} + (\beta + m) \times \tanh^{-1} \frac{l}{R_1 + R_4} - \alpha \tanh^{-1} \frac{m}{R_3 + R_4} - \beta \tanh^{-1} \frac{l}{R_2 + R_3} \right] - \frac{\mu}{4\pi} \frac{\Omega d}{\tan \theta}$$
(14)

$$M_{\text{RDL,diff}} = \sum_{i=1,N+1} \sum_{j=1}^{N} M_{p3}(l_{\text{RDL},t}/2, l_{\text{RDL},b}, d, \alpha_{ij}, \beta_{ij}, \theta) + \sum_{i=2}^{N} \left(\sum_{j=1}^{i-1} + \sum_{j=1}^{N+1-i} \right) \times M_{p3}(l_{\text{RDL},t}, l_{\text{RDL},b}, d, \alpha_{ij}, \beta_{ij}, \theta)$$
(15)
 $(j-1)S_{\text{TSV}} = 0$ (j-1)S_{TSV}(16)

$$a_{ij} = \frac{(j-1)S_{1SV}}{\sin\theta}, \quad \beta_{ij} = \frac{(j-1)S_{1SV}}{\tan\theta}$$
(16)

$$+ 2(M_{\text{TSV},s} + D_{\text{RDL},s}) + 2(M_{\text{TSV},\text{sr}} - M_{\text{TSV},\text{dr}} + M_{\text{RDL},b}) + M_{\text{RDL},t} - M_{\text{RDL},\text{diff}}).$$
(17)

Note that the currents flowing in the TSVs are orthogonal to that in the RDLs, thus the mutual–partial inductance between TSVs and RDLs is zero. The mutual inductance between TSVs from the same row is positive due to the currents with the same direction flowing through them. Conversely, the mutual inductance between TSVs from different rows is negative. Similarly, the RDLs from the top or bottom layer contribute the positive mutual inductance, while the RDLs from different layers contribute to the negative mutual inductance. In addition, every segment in 3-D inductor can be derived by (17), which

TABLE I Comparison of Inductance and Simulation Time From Q3D and Model

	Inductance (nH)			Percent error (%)		Simulation time (sec)		
N	Q3D	Model	Our	Model	Our Old	03D	Model	Our
		[4]	Model	[4]	Model	QSD	[4]	Model
1	0.60	0.53	0.62	11.67	3.33	75	0.014	0.020
2	1.69	1.55	1.70	8.28	0.59	102	0.019	0.028
3	3.05	2.88	3.04	5.57	0.33	172	0.021	0.032
4	4.59	4.43	4.56	3.49	0.65	205	0.025	0.045
5	6.24	6.14	6.18	1.60	0.96	268	0.027	0.049
6	7.97	7.85	7.88	1.51	1.12	286	0.030	0.052
7	9.75	9.92	9.64	1.75	1.13	314	0.032	0.053
8	11.58	11.95	11.43	3.2	1.25	335	0.033	0.055
9	13.46	14.06	13.25	4.46	1.56	365	0.035	0.056
10	15.34	16.22	15.09	5.74	1.63	412	0.038	0.058

is the sum of the self-inductance of every segment and mutual inductance between every two segments.

III. VERIFICATION AND DISCUSSION

In this section, the accuracy of the proposed model is verified by the ANSYS Q3D extractor. Here the simulation model has l_{TSV} of 200 μ m, r_{TSV} of 10 μ m, w_{RDL} of 20 μ m, t_{RDL} of 4 μ m, P_{TSV} of 300 μ m, and S_{TSV} of 40 μ m.

Table I shows the comparison of inductance results and simulation time between Q3D and models that are proposed in [4] and this letter. It can be found that for the inductor with turns from 1 to 10, our model has a higher accuracy and a smaller error fluctuation than the model from [4], the error is less than 3.5%. The reason is that the internal inductance is considered in the calculation of self-inductance and the case of complicated RDL layers with offset and at different planes are considered in the calculation of mutual inductance. Compared with the Q3D extractor, the simulation time is reduced by a large factor using the analytical model from [4] and this letter. For the higher precision, more complex formulas are used in our model, so the simulation time is slightly larger than the model from [4], but there is no effect on the design of 3-D inductor.

As shown in Table I, the error trend of our model is fluctuant. We use terms of a calculation error and method error to explain the fluctuation. The calculation error can be determined by (18), where $\sum L_{\text{partial_simulation}}$ is the sum of the partial inductance of every segment in 3-D inductor obtained by simulation, $\sum L_{\text{partial}_\text{calculation}}$ is the sum of the partial inductance of every segment obtained by calculation. So the error is introduced by the formulas of partial self and mutual inductance of TSV and RDL, which is increasing as the number of turns increase. The method error can be determined by (19), where $L_{\text{total}_\text{simulation}}$ is the total inductance of the 3-D inductor obtained by simulation. The error is introduced by the magnetic variation at the junction of TSV and RDL, it brings about some deviations between the summation of partial inductance and full inductance. The variation of calculation error and method error with turn number (N) is shown in Fig. 3. These two errors have opposite signs and can cancel each other out. So the error trend of our model is first decrease and then increase

Calculation error

$$= \frac{\sum (L_{\text{partial_simulation}}) - \sum (L_{\text{partial_calculation}})}{\sum L_{\text{partial_simulation}}}$$

(18)

Method error =
$$\frac{L_{\text{total_simulation}} - \sum (L_{\text{partial_calculation}})}{L_{\text{total_simulation}}}.$$
 (19)



Fig. 3. Variation of calculation error and method errors with N.



Fig. 4. Comparison of inductance between model and Q3D with various design parameters. (a) l_{TSV} . (b) w_{RDL} . (c) P_{TSV} . (d) $S_{\text{TSV}}(N = 2)$.

Fig. 4 compares the analytical and simulation results of total inductance with various l_{TSV} , w_{RDL} , P_{TSV} , and S_{TSV} . The comparison shows a good agreement over various design parameters ranges, which further verified the accuracy of the model.

IV. CONCLUSION

An analytical model to accurately calculate the total inductance of 3-D inductor based on TSV is proposed in this letter, which provided results in good agreement with the simulation results by Q3D but uses negligible time. When the simulation and analytical results are compared for the inductance of the inductor with turns from 1 to 10, the error is within 3.5%. So our model can be used as a simple and precise method to estimate the inductance of the 3-D inductors. Finally, the reason for the error variation of our model is discussed.

REFERENCES

- D. H. Kim, K. Athikulwongse, and S. K. Lim, "Study of through-siliconvia impact on the 3-D stacked IC layout," *IEEE Trans. Very Large Scale Integr. (VLSI) Syst.*, vol. 21, no. 5, pp. 862–874, May 2013.
- [2] T. Ebefors *et al.*, "The development and evaluation of RF TSV for 3D IPD applications," in *Proc. IEEE Int. 3D Syst. Integr. Conf.*, San Francisco, CA, USA, Oct. 2013, pp. 1–8.
- [3] M. Duplessis, O. Tesson, F. Neuilly, J. R. Tenailleau, and P. Descamps, "Physical implementation of 3D integrated solenoids within silicon substrate for hybrid IC applications," in *Proc. 39th Eur. Microw. Conf.*, Rome, Italy, Sep./Oct. 2009, pp. 1006–1009.
- [4] F. Wang and N. Yu, "Simple and accurate inductance model of 3D inductor based on TSV," *Electron. Lett.*, vol. 52, no. 21, pp. 1815–1816, Oct. 2016.
- [5] Y. Liang and Y. Li, "Closed-form expressions for the resistance and the inductance of different profiles of through-silicon vias," *IEEE Electron Device Lett.*, vol. 32, no. 3, pp. 393–395, Mar. 2011.
- [6] Z. Mei, G. Dong, Y. Yang, J. Zheng, J. Chai, and W. Zhu, "Universal closed-form expression based on magnetic flux density for the inductance of tapered through-silicon vias (T-TSVs)," *Microelectron. J.*, vol. 63, pp. 20–26, May 2017.
- [7] C. R. Paul, *Inductance: Loop and Partial*. New York, NY, USA: Wiley, 2010, pp. 236–239.